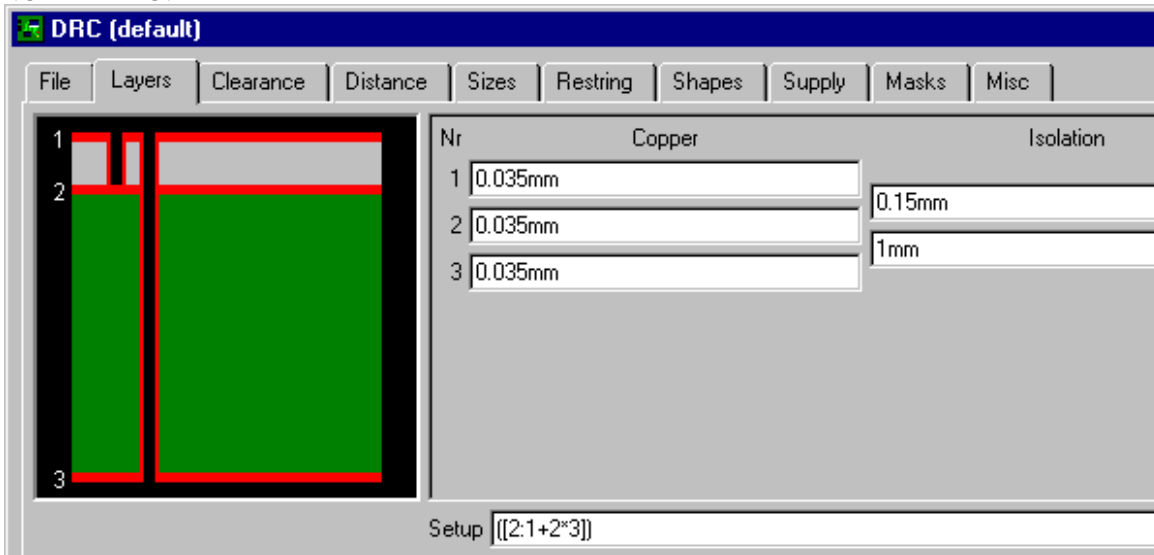


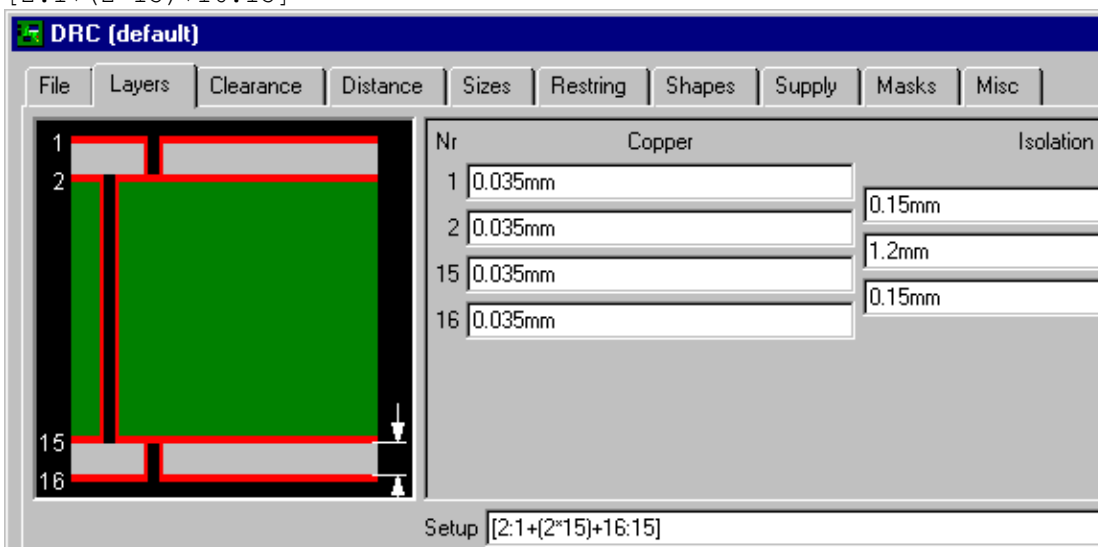
EAGLE Version 4.1 - Design Rules - Layer-Stack

* Core
 + Prepreg
 () Through/Buried Via
 [:] Blind/Micro Via

1 Prepreg - 1 Core :
 1 Blind/Micro -||-||--
 Prepreg || ||
 2 Through/Buried ----||--
 Core ||--
 15 Through/Buried ----||--
 ([2:1+2*3])



1 Prepreg - 1 Core - 1 Prepreg / Variante 1:
 1 Blind/Micro ----||-
 Prepreg ||
 2 Through/Buried -||----
 Core ||----
 15 Through/Buried -||----
 Prepreg ||
 16 Blind/Micro ----||-
 [2:1+(2*15)+16:15]

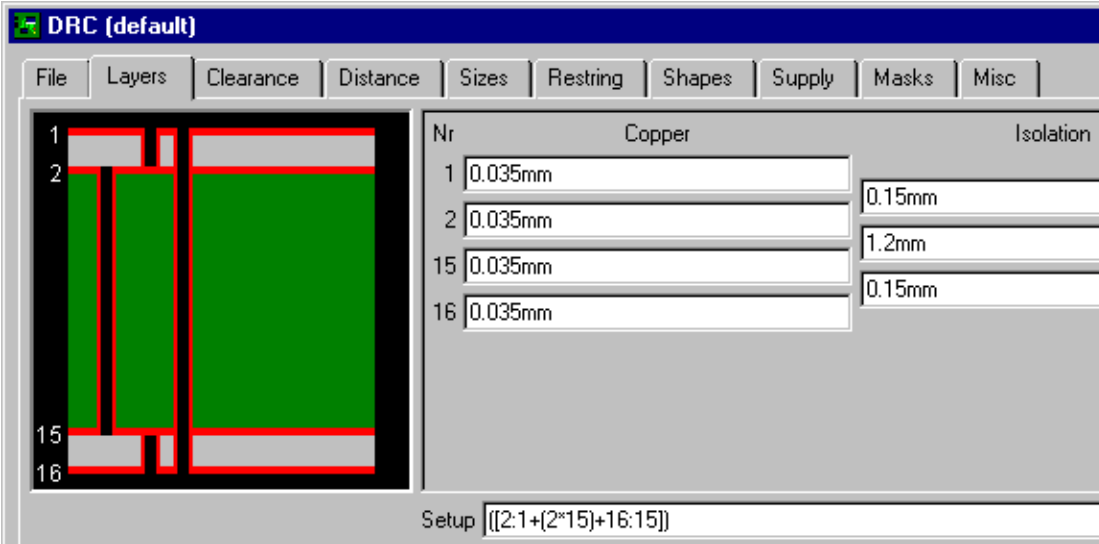


1 Prepreg - 1 Core - 1 Prepreg / Variante 2:

```

1 Blind/Micro    ----||-||-
  Prepreg       ||  ||
2 Through/Buried -||-----||-
  Core          ||    ||
15 Through/Buried -||-----||-
  Prepreg       ||  ||
16 Blind/Micro   ----||-||-
  
```

((2:1+(2*15)+16:15))

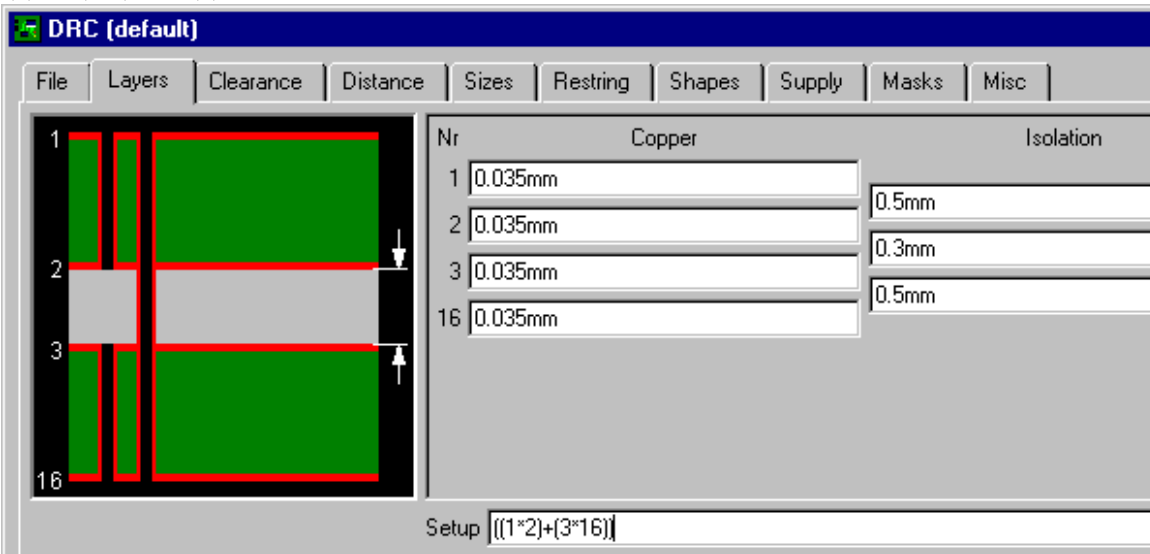


1 Core - 1 Prepreg - 1 Core :

```

1 Through/Buried -||--||-
  Core          ||  ||
2 Through/Buried -||--||-
  Prepreg       ||  ||
3 Through/Buried -||--||-
  Core          ||  ||
16 Through/Buried -||--||-
  
```

((1*2)+(3*16))



2 Prepreg - 1 Core - 1 Prepreg :

```

1 Blind/Micro -----||-||-
  Prepreg           || ||
2 Blind/Micro -----||-||-||-||-
  Prepreg           || ||      ||
13 Through/Buried -||-||-||-||-||-
  Core              || ||      ||
14 Through/Buried -||-||-||-||-||-
  Prepreg           || ||
16 Blind/Micro --- -----||-||-
  
```

((2:1+[13:(2+(13*14))]+16:14))

The screenshot shows the DRC (default) dialog box with the following settings:

Nr	Copper	Isolation
1	0.035mm	0.15mm
2	0.035mm	0.15mm
13	0.035mm	1.5mm
14	0.035mm	.15mm
16	0.035mm	

Setup: ((2:1+[13:(2+(13*14))]+16:14))

2 Prepreg - 1 Core - 2 Prpreg

```

1 Blind/Micro -----||-||-||-
  Prepreg           || ||
2 Blind/Micro -----||-||-||-||-||-
  Prepreg           || || || ||
3 Through/Buried -||-||-||-||-||-||-
  Core              || ||      ||
4 Through/Buried -||-||-||-||-||-||-
  Prepreg           || ||      ||
5 Blind/Micro -----||-||-||-||-||-
  Prepreg           || ||
16 Blind/Micro -----||-||-||-
  
```

((2:1+((3:(2+(3*4))+5:4))+16:5))

The screenshot shows the DRC (default) dialog box with the following settings:

Nr	Copper	Isolation
1	0.035mm	0.15mm
2	0.035mm	0.15mm
3	0.035mm	1mm
4	0.035mm	0.15mm
5	0.035mm	0.15mm
16	0.035mm	

Setup: ((2:1+((3:(2+(3*4))+5:4))+16:5))

1 Prepreg - 2 Core - 1 Prepreg :

```

1 Blind/Micro    ---||-||-----||--
  Prepreg        || ||      ||
2 Through/Buried -----||-----||--
  Core           ||      ||
3 Through/Buried -----||-----||--
  Prepreg        ||      ||
14 Through/Buried -----||-----||--
  Core           ||      ||
15 Through/Buried -----||-----||--
  Prepreg        || || ||
16 Blind/Micro   -----||-||-||--
  
```

((([2:1+2*3])+(14*15+16):15])

Nr	Copper	Isolation
1	0.035mm	0.28mm
2	0.035mm	0.35mm
3	0.035mm	0.28mm
14	0.035mm	0.35mm
15	0.035mm	0.28mm
16	0.035mm	

Setup: (([2:1+2*3])+(14*15+16):15]

2 Prepreg - 1 Core - 1 Prepreg :

```

1 Blind/Micro    -----||-||-
  Prepreg        || ||
2 Blind/Micro    ----||-||-----||-
  Prepreg        || ||
13 Through/Buried -||-----||-----||-
  Core           ||      ||
14 Through/Buried -||-----||-----||-
  Prepreg        ||      ||
16 Blind/Micro   --- -----||-||-
  
```

(([2:1+([13:2+(13*14))]+16:14])

Nr	Copper	Isolation
1	0.035mm	0.15mm
2	0.035mm	0.15mm
13	0.035mm	1mm
14	0.035mm	0.15mm
16	0.035mm	

Setup: ([2:1+([13:2+(13*14))]+16:14])

4 Prepreg - 4 Core - 4 Prepreg / Variante 1:

```

1 Blind/Micro -----||-||-
  Prepreg                      || |
2 Blind/Micro -----||-||-
  Prepreg                      || |
3 Blind/Micro -----||-||-
  Prepreg                      || |
4 Blind/Micro -----||-||-
  Prepreg                      || |
5 Through/Buried -----||-||-
  Core                          || |
6 Through/Buried -----||-||-
  Prepreg                      || |
7 Through/Buried -----||-||-
  Core                          || |
8 Through/Buried -----||-||-
  Prepreg                      || |
9 Through/Buried -----||-||-
  Core                          || |
10 Through/Buried -----||-||-
  Prepreg                      || |
11 Through/Buried -----||-||-
  Core                          || |
12 Through/Buried -----||-||-
  Prepreg                      || |
13 Blind/Micro -----||-||-
  Prepreg                      || |
14 Blind/Micro -----||-||-
  Prepreg                      || |
15 Blind/Micro -----||-||-
  Prepreg                      || |
16 Blind/Micro -----||-||-
  
```

((2:1+[3:2+[4:3+[5:4+(5*6+(7*8+9*10))+11*12]+13:12]+14:13]+15:14]+16:15))

The screenshot shows a software interface for Design Rule Check (DRC). On the left, there is a small window displaying a PCB layout with red and green layers. The main area contains a table with the following columns: 'Nr', 'Copper', and 'Thickness'. The table lists 13 rows of data. At the bottom, there is a 'Setup' field containing a complex mathematical expression.

Nr	Copper	Thickness
1	0.035mm	0.1mm
2	0.035mm	0.1mm
3	0.035mm	0.1mm
4	0.035mm	0.1mm
5	0.035mm	0.5mm
6	0.035mm	0.1mm
7	0.035mm	0.5mm
8	0.035mm	0.1mm
9	0.035mm	0.5mm
10	0.035mm	0.1mm
11	0.035mm	0.5mm
12	0.035mm	0.1mm
13	0.035mm	0.1mm

Setup: ((2:1+[3:2+[4:3+[5:4+(5*6+(7*8+9*10))+11*12]+13:12]+14:13]+15:14]+16:15))

4 Prepreg - 4 Core - 4 Prepreg / Variante 2:

```

1 Blind/Micro -----||-||-
  Prepreg                ||  ||
2 Blind/Micro -----||-||-
  Prepreg                ||  ||
3 Blind/Micro -----||-||-
  Prepreg                ||  ||
4 Blind/Micro -----||-||-
  Prepreg                ||  ||
5 Through/Buried -----||-||-
  Core                   ||  ||
6 Through/Buried -----||-||-
  Prepreg                ||  ||
7 Through/Buried -----||-||-
  Core                   ||  ||
8 Through/Buried -----||-||-
  Prepreg                ||  ||
9 Through/Buried -----||-||-
  Core                   ||  ||
10 Through/Buried -----||-||-
  Prepreg                ||  ||
11 Through/Buried -----||-||-
  Core                   ||  ||
12 Through/Buried -----||-||-
  Prepreg                ||  ||
13 Blind/Micro -----||-||-
  Prepreg                ||  ||
14 Blind/Micro -----||-||-
  Prepreg                ||  ||
15 Blind/Micro -----||-||-
  Prepreg                ||  ||
16 Blind/Micro -----||-||-
  
```

((2:1+[3:2+([4:3+([5:4+(5*6+(7*8+9*10))+11*12))+13:12])+14:13))+15:14]+16:15))

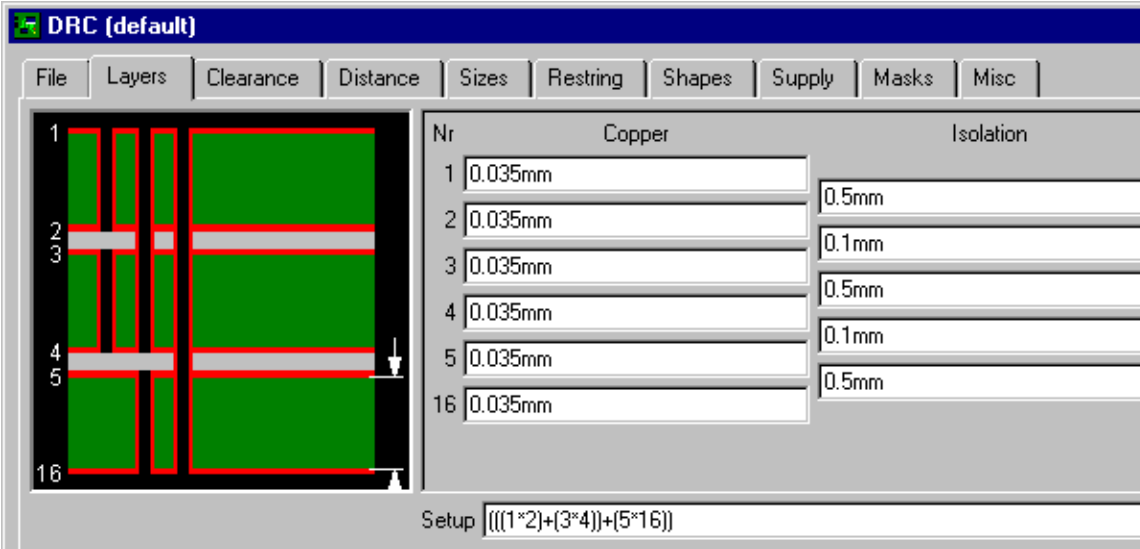
The screenshot shows a software interface for Design Rule Check (DRC). The main window is titled "DRC (default)" and has several tabs: File, Layers, Clearance, Distance, Sizes, Restring, Shapes, Supply, Masks, and Misc. The "Layers" tab is active, showing a preview of a PCB layout with red and green layers. To the right of the preview is a table with the following columns: "Nr", "Copper", and "I:". The table lists 13 rows of specifications for copper layers, with values for thickness and clearance.

Nr	Copper	I:
1	0.035mm	
2	0.035mm	0.1mm
3	0.035mm	0.1mm
4	0.035mm	0.1mm
5	0.035mm	0.1mm
6	0.035mm	0.5mm
7	0.035mm	0.1mm
8	0.035mm	0.5mm
9	0.035mm	0.1mm
10	0.035mm	0.5mm
11	0.035mm	0.1mm
12	0.035mm	0.5mm
13	0.035mm	0.1mm

At the bottom of the interface, there is a "Setup" field containing the same mathematical expression as above: ((2:1+[3:2+([4:3+([5:4+(5*6+(7*8+9*10))+11*12))+13:12])+14:13))+15:14]+16:15))

3 Core / Variante 1:
 1 Through/Buried ---|---|---|---|
 Core || || || ||
 2 Through/Buried ---|---|---|---|
 Prepreg || || ||
 3 Through/Buried ---|---|---|---|
 Core || || || ||
 4 Through/Buried ---|---|---|---|
 Prepreg || ||
 5 Through/Buried ---|---|---|---|
 Core || || || ||
 16 Through/Buried ---|---|---|---|

$$((1*2) + (3*4)) + (5*16)$$



3 Core / Variante 2:
 1 Through/Buried ---|---|---|---|
 Core || || || ||
 2 Through/Buried ---|---|---|---|
 Prepreg || ||
 3 Through/Buried ---|---|---|---|
 Core || || || ||
 4 Through/Buried ---|---|---|---|
 Prepreg || ||
 5 Through/Buried ---|---|---|---|
 Core || || || ||
 16 Through/Buried ---|---|---|---|

$$((1*2) + ((3*4) + (5*16)))$$

