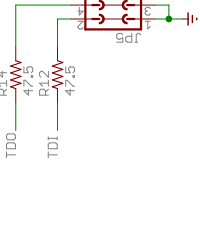
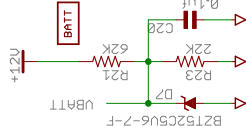
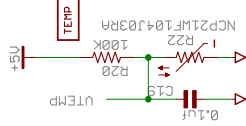
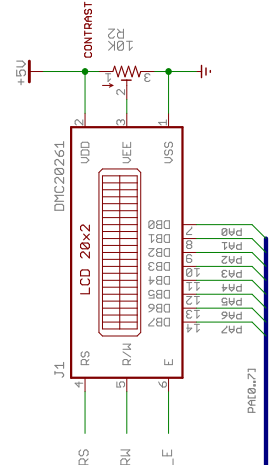
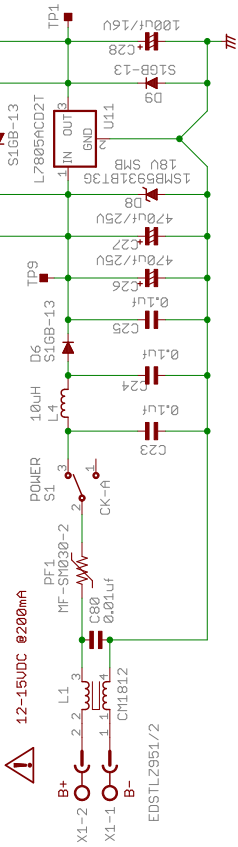


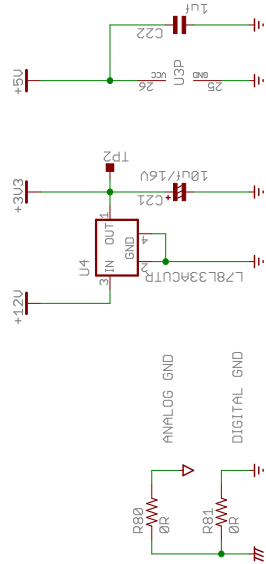
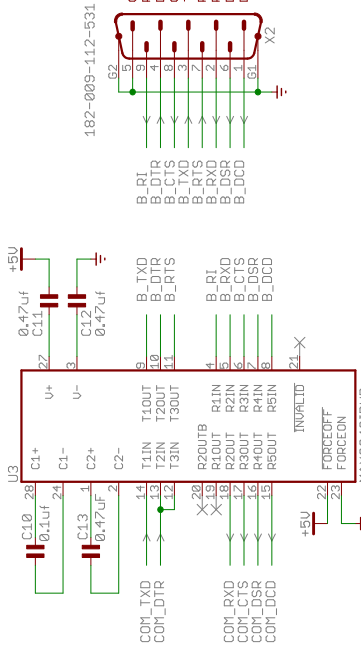
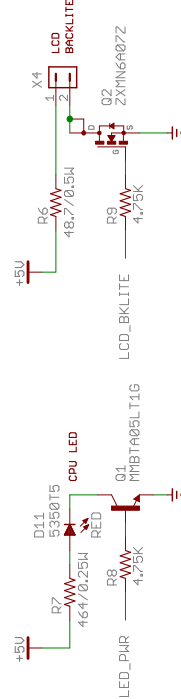
PROPRIETARY



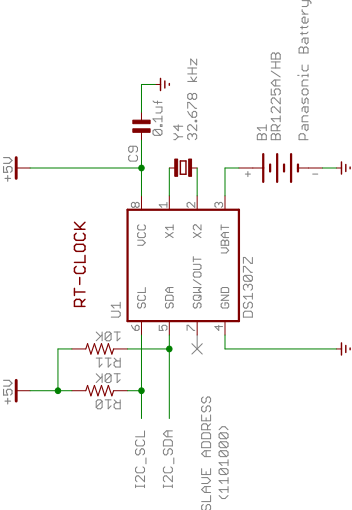
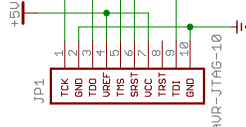
## POWER/REGULATION



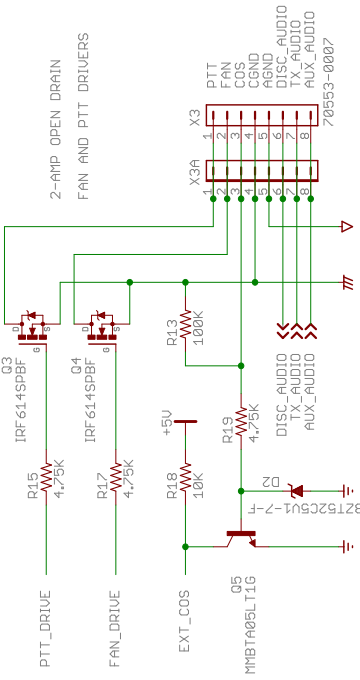
## LED/BACKLIGHT DRIVERS



## MEASUREMENTS



## EXTERNAL RADIO INTERFACE



ASSY 3XX-100	<b>(C)</b>	<b>DATE CODE</b> A0709	<b>ISSUE</b>	<b>KG4LNE</b>	<b>ALPHABETIC, OR USA</b>	<b>THIS DRAWING CONTAINS PROPOSED INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.</b>	<b>(C) 2008</b>
			DRAWN	TITLE			
			R.STARR	MICRO-RC REPEATER CONTROLLER			
			K.LINGERER	DATE	DRG N°		
			DATE	3/20/2009	9:01:47 PM	ED-3XX-01	
				FILE: MicroRC-C	PAGE: 1/3		
LAYER-CHECK-4		PCB-TEXTURE	4/0/10/06				



1										2										3										4										5										6										7										8										9										10									
THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF ROBERT ELECTRONICS, INC. REPRODUCTION IN WHOLE OR IN PART IS STRICTLY PROHIBITED WITHOUT WRITTEN PERMISSION.																																																																																																			
PROPRIETARY																																																																																																			
REV-B <01/25/2007>																																																																																																			
1) Changed CPU from ATmega128 to ATmega1281 for additional SRAM memory <4k->8k>																																																																																																			
2) Changed serial port driver U3 caps C10, C11, C12 and C13 from electrolytic to chip cap 0805 packages.																																																																																																			
3) Added pulldown resistor R13 on COS input of CN3 to prevent base of Q13 from floating.																																																																																																			
4) Added tantalum cap C78 on squelch ADC buffer amp (IC50 output) for low pass filtering of ADC input signal.																																																																																																			
5) Increased expansion I/O connector JP5 pin count to include SPI and I2C buss signals.																																																																																																			
6) Swapped I/O pins PC3 and PD2 to provide an interrupt capable pin on the expansion port. PC3 is now INT_COS and PD2 is now INT_EXP (INT2).																																																																																																			
7) Changed all small electrolytics to tantalum packages.																																																																																																			
8) Swapped trim pot pins 1 & 3 on R52 (output level) and R2 (LCD contrast) to fix incorrect rotation/gain action.																																																																																																			
9) Added U12 for 4.096V precision ADC voltage reference to AVCC.																																																																																																			
10) Added L1 common mode choke and C80 on power input for additional noise protection.																																																																																																			
11) Omitted L5 and L6, replaced with 0R jumpers R80 and R81																																																																																																			
12) Omitted D2, D3 and B1 Goldcap. Changed B1 to BR1225 battery.																																																																																																			
13) Changed Q3 & Q4 from IRF1010 to IRF614 for lower current/cost.																																																																																																			
REV-C <06/21/2007>																																																																																																			
1) Added R82 and R83 to fix op-amp HF oscillation problem.																																																																																																			
2) I/O pin PD2 changed - assigned to ISD_RAC (INT7) for interrupt capable pin.																																																																																																			
3) I/O pin PC0 changed - assigned to COM_DTR.																																																																																																			
4) I/O pin PC1 changed - assigned to PTT_DRIVE.																																																																																																			
5) I/O pin PE2 changed - assigned to EXP_PC0 on expansion port.																																																																																																			
6) I/O pin PE3 changed - assigned to EXP_PC1 on expansion port.																																																																																																			
7) I/O pin PE6 changed - assigned to COM_DCD.																																																																																																			
8) I/O pin PG5 changed - assigned to LCD_BKLITE																																																																																																			
9) I/O pin PG1 changed - assigned to EXT_COS																																																																																																			
10) I/O pin PB4 changed - assigned to COM_CTS																																																																																																			
11) I/O pin PB7 changed - assigned to COM_DSR																																																																																																			
12) I/O pin PC3 changed - assigned to CS_DTMF																																																																																																			
13) I/O pin PF7 changed - assigned jumper J3 to TDI, added R12																																																																																																			
14) Added D2 (B2T52C5V1-7-F) and changed value of D10 to 5.1V (B2T52C5V1-7-F)																																																																																																			
A																																																																																																			
B																																																																																																			
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